

S7D030120Q

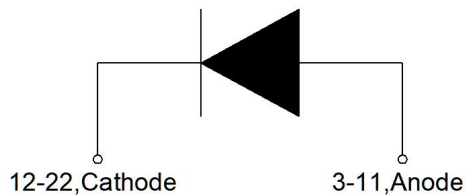
1200V SiC POWER SCHOTTKY RECTIFIER



Description

S7D030120Q is a SiC Schottky rectifier packaged in Q-DPAK(HV) case. The device is a high voltage Schottky rectifier that has very low total conduction losses and very stable switching characteristics over temperature extremes. The S7D030120Q is ideal for energy sensitive, high frequency applications in challenging environments.

Circuit Diagram



Features

- 175°C T_J operation
- Ultra-low switching loss
- Switching speeds independent of operating temperature
- Low total conduction losses
- High forward surge current capability
- High package isolation voltage
- Terminals finish: 100% Pure Tin
- "-A" is an AEC-Q101 qualified device
- Pb - Free Device
- All SMC parts are traceable to the wafer lot
- Additional electrical and life testing can be performed upon request

Applications

- Alternative energy inverters
- Power Factor Correction (PFC)
- Free-Wheeling diodes
- Switching supply output rectification
- Reverse polarity protection

Maximum Ratings:

Characteristics	Symbol	Condition	Max.	Units
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_{DC}	-	1200	V
Average Rectified Forward Current	$I_F (AV)1$	$T_C=25^{\circ}C$	106	A
	$I_F (AV)3$	$T_C=158^{\circ}C$	35	A
Repetitive Peak Forward Surge Current	I_{FRM1}	10ms, Half Sine pulse, $T_C=25^{\circ}C$	162	A
	I_{FRM2}	10ms, Half Sine pulse, $T_C=110^{\circ}C$	97	A
Peak One Cycle Non-Repetitive Surge Current	I_{FSM1}	10ms, Half Sine pulse, $T_C=25^{\circ}C$	248	A
	I_{FSM2}	10ms, Half Sine pulse, $T_C=110^{\circ}C$	216	A
Power Dissipation	P_{tot1}	$T_C=25^{\circ}C$	380	W
	P_{tot2}	$T_C=110^{\circ}C$	164	W

Electrical Characteristics:

Characteristics	Symbol	Condition	Typ.	Max.	Units
Forward Voltage Drop*	V_{F1}	@ 30A, Pulse, $T_J = 25^{\circ}C$	1.5	1.8	V
	V_{F2}	@ 30A, Pulse, $T_J = 175^{\circ}C$	2.2	3.0	V
Reverse Current*	I_{R1}	@ $V_R = \text{rated } V_R$, $T_J = 25^{\circ}C$	1	20	μA
	I_{R2}	@ $V_R = \text{rated } V_R$, $T_J = 175^{\circ}C$	5	200	μA
Junction Capacitance	C_T	$V_R=0V$, $T_J=25^{\circ}C$, $f=1MHz$	1850	-	pF
Reverse Recovery Charge	Q_c	$I_F = 30A$, $di/dt=200A/\mu s$ $V_R = 800V$, $T_J=25^{\circ}C$	151	-	nC
Capacitance Stored Energy	E_C	$V_R = 800V$, $T_J=25^{\circ}C$	45	-	μJ

* Pulse width < 300 μs , duty cycle < 2%

Thermal-Mechanical Specifications:

Characteristics	Symbol	Condition	Specification	Units
Junction Temperature	T_J	-	-55 to +175	°C
Storage Temperature	T_{stg}	-	-55 to +175	°C
Typical Thermal Resistance Junction to Case	$R_{\theta JC}$	DC operation	0.39	°C/W

Ordering Information

Device	Package	Plating	Shipping
S7D030120Q	HV-Q-DPAK	Pure Sn	1000pcs / reel

For information on tape and reel specifications, including part orientation and tape sizes, please refer to our tape and reel packaging specification.

Ratings and Characteristics Curves

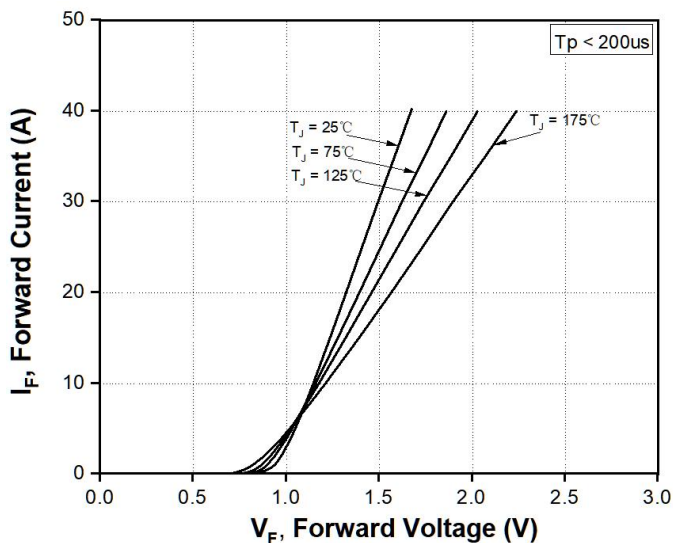


Fig.1-Typical Forward Voltage

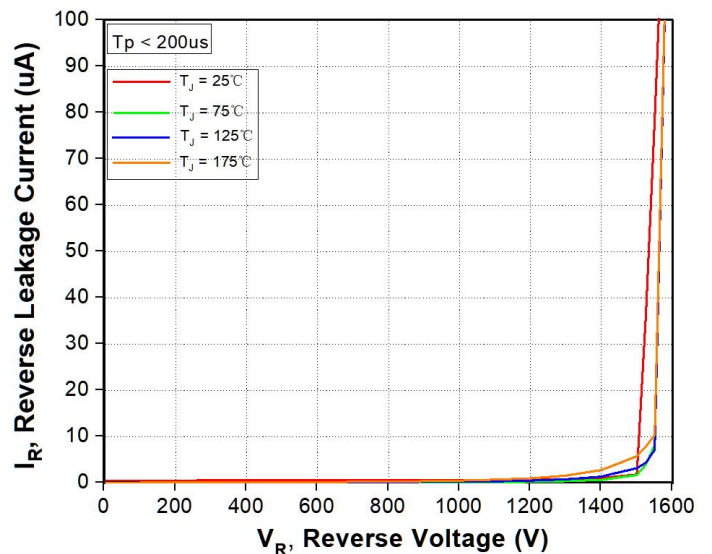


Fig.2-Typical Reverse Characteristics

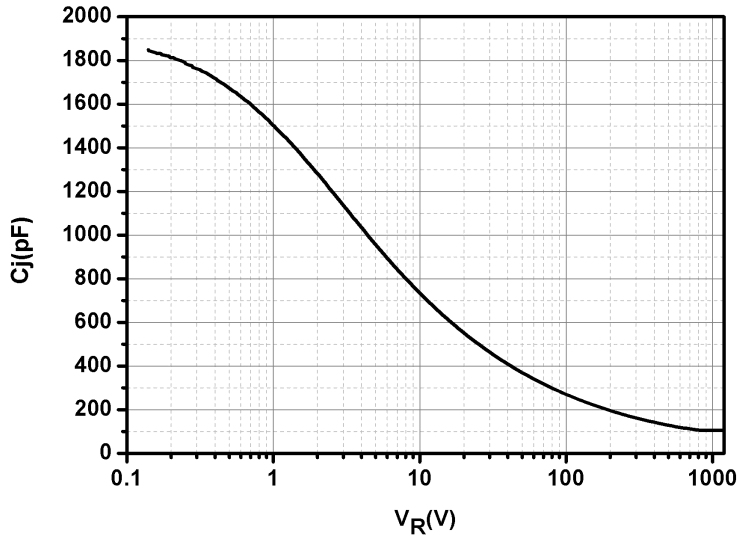


Fig.3-Capacitance vs. Reverse Voltage

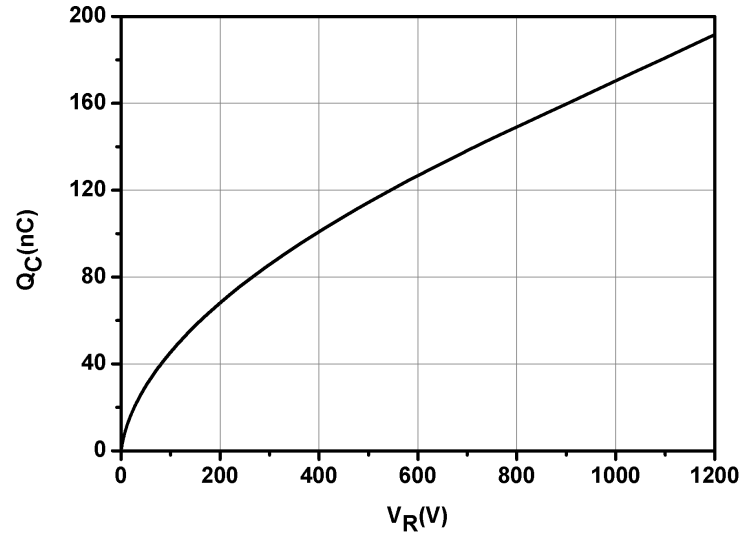


Fig.4-Total Capacitance Charge vs. Reverse Voltage

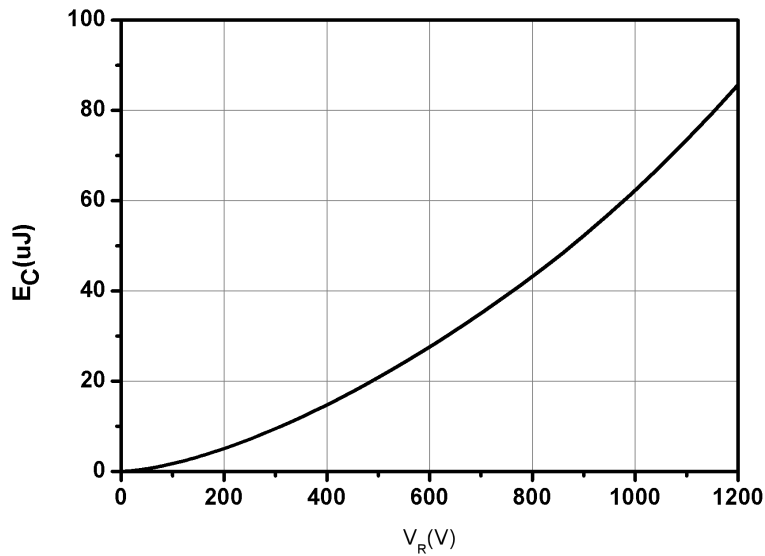


Fig.5-Capacitance Stored Energy

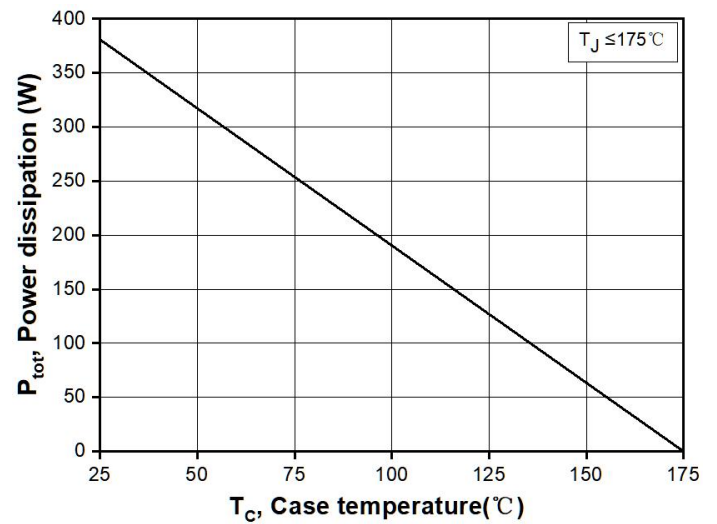


Fig.6-Power Derating

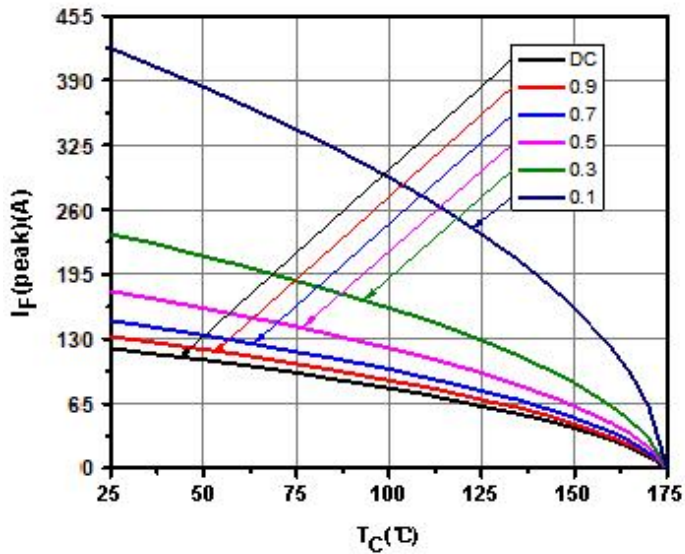
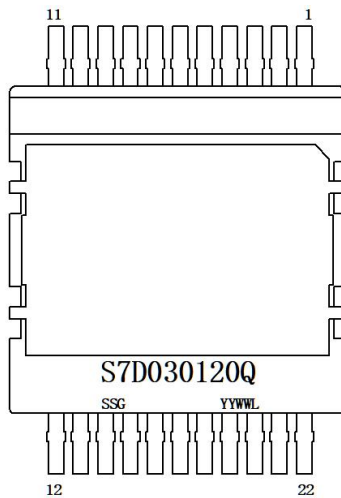


Fig.7-Power Derating

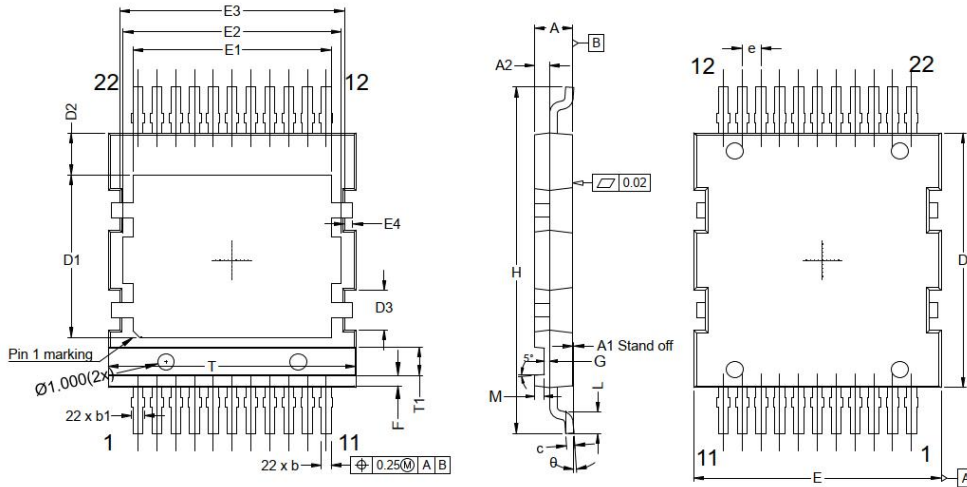
Marking Diagram



S7D = Device Type
Q = Package type
030 = Forward Current (30A)
120 = Reverse Voltage (1200V)
SSG = SSG
YY = Year
WW = Week
L = Lot Number

Cautions: Molding resin
Epoxy resin UL94V-0

Mechanical Dimensions HV-Q-DPAK



COMMON DIMENSIONS

SYMBOL	mm	
	Min	Max
A	2.20	2.35
A1	0.00	0.15
A2	0.80	1.00
b	0.50	0.70
b1	0.50	0.90
c	0.46	0.58
e	1.14	
D	15.30	15.50
D1	9.74	9.94
D2	2.54	
D3	2.40	
E	14.90	15.10
E1	11.91	12.11
E2	13.20	
E3	13.60	
E4	0.50	
H	20.81	21.11
L	1.20	1.40
T	14.90	15.10
T1	1.65	1.75
M	0.60	
F	0.63	
G	0.30	

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